

DONALD R. ANTONELLI  
MELVIN KRAUS  
WILLIAM I. SOLOMON\*  
GREGORY E. MONTONE  
RONALD J. SHORE  
DONALD E. STOUT  
ALAN E. SCHIAVELLI  
JAMES N. DRESSER  
CARL I. BRUNDIDGE\*  
PAUL J. SKWIERAWSKI\*  
HUNG H. BUI\*  
ALFRED A. STADNICKI\*  
\*ADMITTED OTHER THAN VA

LAW OFFICES  
**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

SUITE 1800  
1300 NORTH SEVENTEENTH STREET  
ARLINGTON, VIRGINIA 22209

TELEPHONE (703) 312-6600  
FACSIMILE (703) 312-6666  
[gmontone@antonelli.com](mailto:gmontone@antonelli.com)

OF COUNSEL  
DAVID T. TERRY  
HAROLD A. WILLIAMSON\*  
FREDERICK D. BAILEY  
DAVID C. OREN  
RALPH T. WEBB\*  
STERLING W. CHANDLER\*  
PATENT AGENT  
LARRY N. ANAGNOS

**OFFICIAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: Examiner D. W. Owens  
Art Unit: 2811

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Re: Y. NAKAMURA, et al. - Application No.: 09/708,450  
Our Ref. No.: 501.39149X00

**SUBMISSION OF DOCUMENTS**

Sir:

Applicants hereby submit the attached "Extension of Time" (1 pg), "Credit Card Payment Form" (1 pg) and an "Amendment" (11 pgs) in the above-identified application.

**CERTIFICATE OF TRANSMISSION:**

I hereby certify that the attached "Extension of Time" (1 pg), "Credit Card Payment Form" (1 pg) and an "Amendment" (11 pgs), are being formally filed in the USPTO via Group 2800 Facsimile No. 703-872-9318 on November 18, 2003.

  
Deborah L. Therrien

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501.39149X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Y. NAKAMURA, et al.  
Application No.: 09/708,450  
Filed: November 9, 2000  
For: A SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE AND METHOD THEREOF  
Art Unit: 2811  
Examiner: D. W. Owens

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**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 18, 2003

Sir:

In response to the Office Action dated June 18, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims and the****Remarks are included following the amendments.**